

**Md Farid**

Lower Mawprem Pdengshnong, Shillong

Phone: +91 8794866645

Email: ansarifarid13@gmail.com

Linkedin: linkedin.com/in/md-farid-a87291119

EDUCATION

- BTech Electronics & Communication Engineering (7.67 / 10)
National Institute of Technology Meghalaya
2014 -2018
- HSSLC (10+2) Science (72.2 / 100)
St Anthony's Higher Secondary School
2011-2013
- SSLC (74.6 / 100)
Mawprem Modern Higher Secondary School
2000-2011

CERTIFICATIONS/TRAINING/PROGRAMMES ATTENDED

- 3rd IEEE Workshop on Advanced MATLAB Application organized by IIT, Guwahati.
- Workshop on Ethical Hacking & Cyber Forensic organized by Byte Code Cyber Security at NIT, Meghalaya.
- Summer Training in "Basic java programming" organized by Ramakrishna Mission computer training unit, Shillong.

INTERNSHIP/TRAINING/PROJECTS

- Pursued 3 weeks of industrial training at Doordarshan Kendra, Shillong.
- Successfully completed a project on "FPGA interface for Serial 20-bit DAC" at Variable Energy Cyclotron Centre a Research & Development unit Department of Atomic Energy.
Project details: The project aim was to develop a high-speed technique for displaying a representation of a cross-section through a human body or other solid objects. For this purpose AD5791 device was used using VHDL on a Xilinx platform. An LVDS technique was used in order to avoid any signal interference as the signal power was very low, as well as noise power, was low which made it difficult to detect the desired signal
- Worked on "Image Compression" using MATLAB.
- Successfully completed a project on "Approximate Divider Circuits" using Cadence and MATLAB under the guidance of (L) Dr. Vinay Kumar, as a part of the final year BTech Project.
Project Details: The project aims to address the problem of power consumption of a divider circuit by developing an approach by approximation technique using Cadence virtuoso on 45nm

technology node. Further the design was tested on image processing applications which were coded on MATLAB.

TECHNICAL SKILLS

- Languages – C, VHDL, Java, Embedded C, MATLAB, Assembly language.
- Familiar with Cadence, Modelsim-Altera, PSpice, MultiSim, ANSYS, Xilinx, Audrino IDE.
- Operating Systems/Platforms –Windows, Linux, Ubuntu
- Application software - MS-Office

WORK EXPERIENCE

- 27th June 2018 to 4th September 2019: National Institute of Electronics & Information Technology, Shillong.
- Position Held: Technical Assistant
Duties performed: Conducted training for state government employees of Meghalaya under the Ministry of Development of North Eastern Region Project. Worked on the Internet of things and also provided training to the students of different universities on IoT. Guided an MCA student for his final year project based on the IoT. Taught and conducted training on different courses offered by NIELIT.

PERSONAL PROFILE

Father's Name	: Jan Md Ansari
Mother's name	: Sairul Begum
Gender	: Male
Marital Status	: Unmarried
Date of birth	: 31st August 1996
Permanent Address	: Lower Mawprem, Pdengshnong near taxi stand Shillong-793002.

PERSONAL SKILLS

Initiator, Industrious, Punctual, Honest, Reliable, Good Workmanship, Well-natured, Studious, Obedient, and Polite. (Acquired during life and career but not necessarily covered on certificate and Awards).

HOBBIES

- Football
- Cooking

DECLARATION

I hereby declare that everything mentioned above is true and correct to the best of my know-how.